

WEST Search History

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DATE: Friday, October 15, 2004

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L63	nix.in. and L62	4
<input type="checkbox"/>	L62	bartkowiak.in. and advanced.as.	40
		<i>DB=EPAB; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L61	bartkowiak.in. and advanced.as.	27
<input type="checkbox"/>	L60	EP-1261914-A1.did.	0
<input type="checkbox"/>	L59	EP-1261914-A1.did.	0
<input type="checkbox"/>	L58	EP-1261914-A1.did.	0
<input type="checkbox"/>	L57	WO-200167238-A1.did.	0
<input type="checkbox"/>	L56	WO-200167238-A1.did.	0
		<i>DB=PGPB; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L55	US-20020019928-A1.did.	1
		<i>DB=DWPI; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L54	saulsbury.in.	30
		<i>DB=USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L53	L43 and L52	4
<input type="checkbox"/>	L52	array with bound\$5	6391
<input type="checkbox"/>	L51	6014723.PN.	1
		<i>DB=DWPI; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L50	trap and L49	3
<input type="checkbox"/>	L49	chaudhry.in. and tremblay.in.	42
		<i>DB=EPAB; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L48	chaudhry.in. and tremblay.in.	12
		<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L47	trap instruction with zero	42
<input type="checkbox"/>	L46	trap adj zero	14
<input type="checkbox"/>	L45	trap with zero	875
<input type="checkbox"/>	L44	multiple bit boundary value	0
<input type="checkbox"/>	L43	6014723.uref.	10
<input type="checkbox"/>	L42	bartkowiak.in. and advanced.asn.	40
<input type="checkbox"/>	L41	L32 with flag\$1	101
<input type="checkbox"/>	L40	L32 with L39	3

<input type="checkbox"/>	L39	implicit	17484
<input type="checkbox"/>	L38	L32 same L37	0
<input type="checkbox"/>	L37	implicit operand\$1	70
<input type="checkbox"/>	L36	L32 with L35	18
<input type="checkbox"/>	L35	destination register	3023
<input type="checkbox"/>	L34	L32 with index	19
<input type="checkbox"/>	L33	L32 with first with second with destination	4
<input type="checkbox"/>	L32	L27 or L28 or L29 or L31	896
<input type="checkbox"/>	L31	compare macro-instruction\$1	1
<input type="checkbox"/>	L30	compare micro-instruction\$1	0
<input type="checkbox"/>	L29	compare macroinstruction\$1	1
<input type="checkbox"/>	L28	compare microinstruction\$1	6
<input type="checkbox"/>	L27	compare instruction\$1	891
<input type="checkbox"/>	L26	L22 and base address	1
<input type="checkbox"/>	L25	802196.ap.	2
<input type="checkbox"/>	L24	r7 and L23	0
<input type="checkbox"/>	L23	vliw and L22	1
<input type="checkbox"/>	L22	6542990.pn.	1
<input type="checkbox"/>	L21	4760374.uref.	18
<input type="checkbox"/>	L20	L5 and L19	17
<input type="checkbox"/>	L19	L17 or L18	65
<input type="checkbox"/>	L18	(4760374 5568624 5878245 6014723 6185673 6298365 20020029332)! [uref]	58
<input type="checkbox"/>	L17	(4760374 5568624 5878245 6014723 6185673 6298365 20020029332)! [pn]	7
<input type="checkbox"/>	L16	6542990.pn.	1
<input type="checkbox"/>	L15	6542990.uref.	0
<input type="checkbox"/>	L14	6014723.uref.	10
<input type="checkbox"/>	L13	(PROCESSOR WITH ACCELERATED ARRAY ACCESS BOUNDS CHECKING).ti.	1
<input type="checkbox"/>	L12	L7 and L9 and L10	1
<input type="checkbox"/>	L11	L5 and L9 and L10	44
<input type="checkbox"/>	L10	index with array	10599
<input type="checkbox"/>	L9	length with array	29164
<input type="checkbox"/>	L8	array same L7	3
<input type="checkbox"/>	L7	L5 same L6	131
<input type="checkbox"/>	L6	less with length	145339
<input type="checkbox"/>	L5	compar\$5 with greater with equal	11721
<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L4	array index with valid	0
<input type="checkbox"/>	L3	pointer with array with valid	4

DB=PGPB,USPT; PLUR=NO; OP=ADJ

<input type="checkbox"/>	L2	pointer with array with valid	132
<input type="checkbox"/>	L1	array index with valid	17

END OF SEARCH HISTORY